Nazarovsky Alexander Evgenievich

Hardware/Software engineer, Metrology specialist, R&D of algorithms and devices for Smart Grids, telecommunications and measurements

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1. Brief: Proficient in measurements, sensors and metrology. Solid experience with signal acquisition and processing systems, hardware/software interfacing, electronic design. Involvement in the DSP algorithm design and computer science. Knowledge of the telecommunication and data transmission technologies. Teamwork skills, information gathering skills, ability to solve difficult design and implementation problems in short time.

2. Education: M.Sc., Ph.D.

1998-2003 – M.Sc., Yaroslavl State University, Radiophysics and electronics

2003-2007 – Ph.D., Yaroslavl State University, 05.12.13 Systems, Means and Devices for Telecommunications. Thesis «Improvement of Algorithms for Double Talk Detection and Handling in PSTN Line Echo-Cancellers» (Vladimir State University, 2007).

3. Professional experience:

Time and Company	Professional experience
09.1998-06.2003 student Yaroslavl State University	• Development of algorithms and programs for digital signal processing in Delphi, C, Assembler and Matlab for PC x86, Texas Instruments and Analog Devices DSP (image processing/filtering, echo cancellation)
09.2003-06.2007 post-graduate Yaroslavl State University	• Research work on adaptive echo cancellation algorithms • Development and implementation of DSP algorithms for echo cancellation and double-talk detection (C, Matlab) • Gave a course on "Operating Systems" for students
02.2004-11.2007 Engineer, JSC "CenterTelecom", Yaroslavl	• LAN and computer park administration • Tech support • Automation scripts and programs writing (BAT files, Delphi)
12.2007-02.2009 IT administrator, JSC "Auto-RR", Yaroslavl	• LAN and computer park administration • Tech support • Company web-site development (HTML)
06.2010-04.2011 IT specialist, JSC "Russian Railroads", Yaroslavl	• Cyber-security and information security works • Electronic digital signature infrastructure support • Development of information security internal database (PHP, JS)
04.2011-present time Lead engineer JSC Engineering center "Continuum", Yaroslavl	• Development of DSP and metrological algorithms for Smart Grid devices (IEC 61850): e.g. Synchronized Phasor Measurement Unit (PMU),Stand Alone Merging Unit (SAMU) • Implementation of algorithms for Smart Grid Ethernet frame latency measurement complex and traffic analyzer (for FGC UES). • Head of metrology algorithm development for IEC 61000-4-30 class A power quality analyzer, including resampling, Fourier harmonic analysis, flickermeter, dip, sag and swell detection, sensor calibration • Metrology design of high-voltage measurement system for ITER project • Modeling and simulation of devices and power networks. Metrological works, development of calibration techniques, testing, writing reports and technical documentation, working with regulatory documents (IEC, IEEE, GOST)

4. Technical experience:

Modeling of complex devices and systems, development of algorithms for data and signal processing, data analysis, metrological and requirements testing, working with measurement equipment. Knowledge of linear algebra, DSP, filter design, numerical methods, telecommunications technologies. Programming: Matlab/Simulink, Labview, Python/Numpy, C. Office: MS Word, Excel, PowerPoint, Visio, Outlook, Photoshop. Other experience: TCP/IP, HTML, PHP, Linux, Arduino/Processing.